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GP-GPU for DFN flow simulations

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The present work proposes the use of General-Purpose Graphics Processing Units (GP-GPUs) to solve flow problems in large scale Discrete Fracture Networks (DFNs). Discrete fracture networks are randomly generated sets of planar polygons in the three dimensional space resembling the fractures in the subsoil. Recently a minimization approach was developed to tackle the issue of effective flow simulations in intricate networks of fractures [1,2,3,4]. If the minimization problem is solved via a gradient based approach, the computation of the descent direction at each iteration requires several matrix-vector operations that involve quantities related to each fracture in the network and that can be performed in parallel. Thus, the use of GPUs appears very promising to accelerate computations. In this context, one of the principal bottlenecks consists in the communications between the GPU and the main-board through the bus PCI-Express. The code is organized in a way that minimizes these communications, allowing, at the same time to optimize the use of all cores in the graphic unit when performing floating-points operations. The code has been developed for GPUs with CUDA (NVIDIA) architecture using the cuSparse and cuBlas libraries to handle operations on both sparse and dense matrices. Experiments show that maximum speed-ups are mainly affected by the number of intersections in the considered DFNS with speed-ups approaching 20x in networks with a large number of intersections.

References

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